

	Type	L #	Hits	Search Text	DB
1	BRS	L2	2	1 with silicon	USPAT
2	BRS	L1	43	infrared adj shield	USPAT
3	BRS	L3	15	infrared adj shielding adj layer	USPAT
4	BRS	L4	4	IR adj shielding adj layer	USPAT
5	BRS	L5	19	IR adj shield	USPAT
6	BRS	L6	20	IR adj shielding	USPAT
7	BRS	L7	89075	cpu with memory	USPAT
8	BRS	L8	689	7 and (light adj shield\$4)	USPAT
9	BRS	L9	198	8 and (infrared or ir)	USPAT
10	BRS	L10	3067	7 and (chip adj2 chip)	USPAT
11	IS&R	L11	1329	(257/777).CCLS.	USPAT
12	BRS	L12	91	11 and (IR or infrared)	USPAT
13	BRS	L13	5	12 and (doped with substrate)	USPAT
14	BRS	L14	40	11 and (doped with substrate)	USPAT
15	BRS	L15	135	(silicon adj layer) with shield	USPAT
16	BRS	L16	25	(silicon adj layer) with shield with light	USPAT
17	BRS	L17	1	("5714790").PN.	US-PGPUB USPAT; U OCR
18	BRS	L18	3	("6229165").URPN.	USPAT